

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2	"20020093862".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/09 14:49
L2	2846	714/718.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/09 14:52
L3	3814	365/200.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/09 14:50
L4	3251	365/201.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/09 14:50
L5	1686	714/25.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/09 14:50
L6	636	714/42.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/09 14:50
L7	269	714/54.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/09 14:50
L8	134	714/702.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/09 14:51
L9	59	(714/718.ccls.) and (memory with test\$3) and (integrated adj circuit) and ((test\$3 with generat\$3) same control\$4) and (test\$3 with mode with (outside or external)) and (control\$4 with (outside or external))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/09 15:12

L10	52	(714/718.ccls.) and (memory with test\$3) and (integrated adj circuit) and (test\$3 with generat\$3 with control\$4) and (test\$3 with mode with (outside or external)) and (control\$4 with (outside or external))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/09 15:21
L11	1	(714/718.ccls.) and (memory with test\$3) and (integrated adj circuit) and ((test\$3 adj signal adj generat\$3) with control\$4) and (test\$3 with mode with (outside or external)) and (control\$4 with (outside or external))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/09 15:38
L12	1	(714/718.ccls.) and (memory with test\$3) and (integrated adj circuit) and ((test\$3 adj signal adj generat\$3) with control\$4) and (control\$4 with (outside or external))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/09 15:39
L13	0	(714/718.ccls.) and (memory with test\$3) and (integrated adj circuit) and ((test\$3 adj signal adj generat\$3) with (control\$4 adj circuit)) and (control\$4 with (outside or external))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/09 15:39
L14	8	(memory with test\$3) and (integrated adj circuit) and ((test\$3 adj signal adj generat\$3) with (control\$4 adj circuit)) and (control\$4 with (outside or external))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/09 16:26
L15	1	(test\$3 adj set\$3 adj mode) with (test\$3 adj execut\$3 adj mode) with switch\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/09 16:31
L16	1	(test\$3 adj set\$3) with (test\$3 adj execut\$3) with switch\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/09 16:29
L17	1	(test\$3 adj (set\$3 or init\$10) adj mode) with (test\$3 adj execut\$3 adj mode) with switch\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/09 16:31
L18	1	(test\$3 adj (set\$3 or init\$10)) with (test\$3 adj execut\$3) with switch\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/09 16:32

L19	4	((test\$3 adj (set\$3 or init\$10)) with (test\$3 adj execut\$3)) same switch\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/09 16:36
L20	10	((test\$3 adj (set\$3 or init\$10)) with (test\$3 adj execut\$3)) same (switch\$3 or chang\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/09 16:41
L21	3	((test\$3 adj (set\$3 or init\$10)) with (test\$3 adj execut\$3)) with (switch\$3 or chang\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/09 16:38
L22	1	((test\$3 adj (set\$3 or init\$10)) with (test\$3 adj execut\$3)) and (memory adj test\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/09 16:44
L23	0	(test\$3 adj (set\$3 or init\$10)) same (test\$3 adj (execut\$3 or conduct\$3 or perform\$3)) same ((switch\$3 or chang\$3) adj mode)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/09 16:47
L24	87	((test\$3 adj (set\$3 or init\$10)) same (test\$3 adj (execut\$3 or conduct\$3 or perform\$3))) and (memory adj test\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/09 16:48
L25	7	((test\$3 adj (set\$3 or init\$10)) same (test\$3 adj (execut\$3 or conduct\$3 or perform\$3)) same mode) and (memory adj test\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/09 16:55
L26	87	((test\$3 adj (set\$3 or init\$10)) same (test\$3 adj (execut\$3 or conduct\$3 or perform\$3))) and (memory adj test\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/09 16:56
L27	63	((test\$3 adj (set\$3 or init\$10)) same (test\$3 adj (execut\$3 or conduct\$3 or perform\$3))) and (test\$3 with mode) and (memory adj test\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/09 16:58
L28	47	((test\$3 adj (set\$3 or initiali\$6)) same (test\$3 adj (execut\$3 or conduct\$3 or perform\$3))) and (test\$3 with mode) and (memory adj test\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/09 16:59

L29	8	((test\$3 adj ((set\$3 adj up) or initiali\$6)) same (test\$3 adj (execut\$3 or conduct\$3 or perform\$3))) and (test\$3 with mode) and (memory adj test\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/09 18:33
L30	530	(chip adj select\$3) and (memory adj test\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/09 18:34
L31	66	((chip adj select\$3) with signal with generat\$3) and (memory adj test\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/09 19:09
L32	3	((increment\$3 adj decrement\$3) adj address\$2) and (memory adj test\$3) and (data with reverse)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/09 19:55
L33	54	((output adj data) with selector with memory) and (memory adj test\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/09 19:57
L34	3	((output adj data) with selector with (selected adj memory)) and (memory adj test\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/09 19:57